



Inventor: John T. Moore

Title: Method of Forming Oxide Regions Over Semiconductor Substrates

Assignee: Micron Technology, Inc.

INFORMATION DISCLOSURE STATEMENT

References – See Attached Form PTO-1449

The attached form PTO-1449 is submitted in compliance with 37 CFR §1.56. No admission is made regarding whether any of the submitted references is prior art. Copies of the references are attached.

Respectfully submitted,

Dated:

May 12, 2004

Attorney:

Jennifer J. Taylor
Jennifer J. Taylor, Ph.D.
Reg. No. 48,711

Form PTO-1449		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY. DOCKET NO. MI22-1384	SERIAL NO. 09/602.395			
<div style="position: relative; height: 100px;"> <div style="position: absolute; top: 0; left: 0; width: 100%; height: 100%; border: 2px solid black; border-radius: 50%; text-align: center; line-height: 100px; font-size: 24px; font-weight: bold;"> O I P E J U C A S MAY 12 2004 PATENT & TRADEMARK OFFICE </div> </div>				LIST OF ART CITED BY APPLICANT (Use several sheets if necessary)				
				APPLICANT John T. Moore				
				FILING DATE June 22, 2000	GROUP 2813			
U.S. PATENT DOCUMENTS								
*Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate		
AA	6,100,163	08-2000	Jang et al.					
AB	6,399,520 B1	06-2002	Kawakami et al.					
AC								
AD								
AE								
AF								
AG								
AH								
AI								
AJ								
AK								
AL								
FOREIGN PATENT DOCUMENTS								
		Document Number	Date	Country	Class	Subclass	Translation	
							Yes	No
	AM							
	AN							
	AO							
	AP							
	AQ							
OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.)								
	AR							
	AS							
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.								